

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Tsu-Jae King

Application No.: 10/757,245

Group No.: 2815

Filed: 1/14/2004

Examiner: Edward Joseph Wojciechowicz

For: VARIABLE THRESHOLD SEMICONDUCTOR DEVICE AND METHOD OF OPERATING...

Patent No.: 6,969,894 B2

Issue date: 11/29/2005

Name of Inventor(s): Tsu-Jae King

Invention Title: VARIABLE THRESHOLD SEMICONDUCTOR DEVICE AND METHOD OF...

Mail Stop Petition

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

PETITION TO CORRECT NAME OF ASSIGNEE ON PATENT ISSUED

1. Petition is hereby made to correct/supply the name of the assignee that was incorrectly designated.
2. The correct name of the assignee is **Progressant Technologies, Inc.**
3. Proof of the correct name of the assignee is established by the assignment document submitted to the Office on 8/2/2004, Reel 014926, Frame 0849. A copy of this assignment document is attached.
4. The petition fee (37 C.F.R. § 1.17(h)) is paid as follows:

Authorization is hereby made to charge the amount of \$130.00 to Deposit Account No.50-0574.

Charge any additional fees required by this paper or credit any overpayment in the manner authorized above. A duplicate of this paper is enclosed.

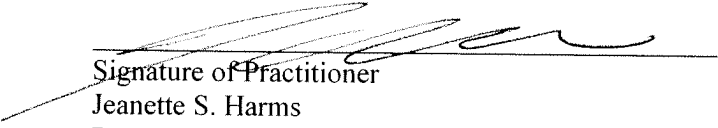
5. Please forward the disposition of this petition so that, if it is favorable, but the patent is already issued with the incorrect name of the assignee, a proper certificate of correction may be submitted.

Date: August 7, 2006

Reg. No.: 35,537

Tel. No.: (408) 451-5907

Customer No.: 35273


Signature of Practitioner

Jeanette S. Harms

Bever, Hoffman & Harms, LLP

2099 Gateway Place, Suite 320

San Jose, CA 95110-1017



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Company: 726 DUBOCE AVENUE
Fax Number: 14153559006
Voice Phone:

Fax Notes:

Pg#	Description
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1	Cover Page
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2	113.TXT
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8	Document 1, Batch 338629
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AUGUST 03, 2004

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* 700103087A*

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726 DUBOCE AVENUE
SAN FRANCISCO, CALIFORNIA 94117

UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 703-308-9723. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, ASSIGNMENT DIVISION, BOX ASSIGNMENTS, CG-4, 1213 JEFFERSON DAVIS HWY, SUITE 320, WASHINGTON, D.C. 20231.

RECORDATION DATE: 08/02/2004

REEL/FRAME: 014926/0849
NUMBER OF PAGES: 6

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

KING, TSU-JAE

DOC DATE: 07/30/2004

ASSIGNOR:

LIU, DAVID K.Y.

DOC DATE: 07/30/2004

ASSIGNEE:

PROGRESSANT TECHNOLOGIES, INC.
470 TUMBLEWEED CT.
FREMONT, CALIFORNIA 94539

SERIAL NUMBER: 10185247

FILING DATE: 06/28/2002

PATENT NUMBER:

ISSUE DATE:

TITLE: ENHANCED READ & WRITE METHODS FOR NEGATIVE DIFFERENTIAL RESISTANCE
(NDR)BASED MEMORY DEVICE

014926/0849 PAGE 2

SERIAL NUMBER: 10185559 FILING DATE: 06/28/2002
PATENT NUMBER: ISSUE DATE:
TITLE: NEGATIVE DIFFERENTIAL RESISTANCE (NDR) BASED MEMORY DEVICE WITH
REDUCED BODY EFFECTS

SERIAL NUMBER: 10185568 FILING DATE: 06/28/2002
PATENT NUMBER: ISSUE DATE:
TITLE: NEGATIVE DIFFERENTIAL RESISTANCE (NDR) ELEMENTS & MEMORY DEVICE
USING THE SAME

SERIAL NUMBER: 10215137 FILING DATE: 08/08/2002
PATENT NUMBER: ISSUE DATE:
TITLE: SILICON ON INSULATOR (SOI) NEGATIVE DIFFERENTIAL RESISTANCE (NDR)
BASED MEMORY DEVICE WITH REDUCED BODY EFFECTS

SERIAL NUMBER: 10314510 FILING DATE: 12/09/2002
PATENT NUMBER: ISSUE DATE:
TITLE: CHARGE TRAPPING DEVICE & METHOD OF FORMING THE SAME

SERIAL NUMBER: 10314735 FILING DATE: 12/09/2002
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD OF FORMING A NEGATIVE DIFFERENTIAL RESISTANCE DEVICE

SERIAL NUMBER: 10314785 FILING DATE: 12/09/2002
PATENT NUMBER: ISSUE DATE:
TITLE: PROCESS FOR CONTROLLING PERFORMANCE CHARACTERISTICS OF A NEGATIVE
DIFFERENTIAL RESISTANCE (NDR) DEVICE

SERIAL NUMBER: 10321031 FILING DATE: 12/17/2002
PATENT NUMBER: ISSUE DATE:
TITLE: INTEGRATED CIRCUIT HAVING NEGATIVE DIFFERENTIAL RESISTANCE (NDR)
DEVICES WITH VARIED PEAK-TO-VALLEY RATIOS (PVRS)

SERIAL NUMBER: 10321090 FILING DATE: 12/17/2002
PATENT NUMBER: ISSUE DATE:
TITLE: ADAPTIVE NEGATIVE DIFFERENTIAL RESISTANCE DEVICE

SERIAL NUMBER: 10322080 FILING DATE: 12/17/2002
PATENT NUMBER: ISSUE DATE:
TITLE: METHODS OF TESTING/STRESSING A CHARGE TRAPPING DEVICE

SERIAL NUMBER: 10324485 FILING DATE: 12/20/2002
PATENT NUMBER: ISSUE DATE:
TITLE: N-CHANNEL PULL-UP ELEMENT & LOGIC CIRCUIT

SERIAL NUMBER: 10753948 FILING DATE: 01/07/2004
PATENT NUMBER: ISSUE DATE:
TITLE: CHARGE TRAPPING DEVICE

SERIAL NUMBER: 10754229 FILING DATE: 01/08/2004
PATENT NUMBER: ISSUE DATE:
TITLE: CMOS COMPATIBLE PROCESS FOR MAKING A CHARGE TRAPPING DEVICE

014926/0849 PAGE 3

SERIAL NUMBER: 10757245 FILING DATE: 01/14/2004
PATENT NUMBER: ISSUE DATE:
TITLE: VARIABLE THRESHOLD SEMICONDUCTOR DEVICE AND METHOD OF OPERATING SAME

SERIAL NUMBER: 10760090 FILING DATE: 01/15/2004
PATENT NUMBER: ISSUE DATE:
TITLE: CMOS PROCESS COMPATIBLE, TUNABLE NEGATIVE DIFFERENTIAL RESISTANCE
(NDR) DEVICE AND METHOD OF OPERATING SAME

SERIAL NUMBER: 10827713 FILING DATE: 04/19/2004
PATENT NUMBER: ISSUE DATE:
TITLE: NEGATIVE DIFFERENTIAL RESISTANCE PULL UP ELEMENT

SERIAL NUMBER: 10827787 FILING DATE: 04/19/2004
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD OF MAKING MEMORY CELL UTILIZING NEGATIVE DIFFERENTIAL
RESISTANCE DEVICES

SERIAL NUMBER: 10827886 FILING DATE: 04/19/2004
PATENT NUMBER: ISSUE DATE:
TITLE: NEGATIVE DIFFERENTIAL RESISTANCE LOAD ELEMENT

SERIAL NUMBER: 10828356 FILING DATE: 04/19/2004
PATENT NUMBER: ISSUE DATE:
TITLE: CHARGE TRAPPING PULL UP ELEMENT

SERIAL NUMBER: 10831867 FILING DATE: 04/26/2004
PATENT NUMBER: ISSUE DATE:
TITLE: NEGATIVE DIFFERENTIAL RESISTANCE (NDR) MEMORY DEVICE WITH REDUCED
SOFT ERROR RATE

SERIAL NUMBER: 10831950 FILING DATE: 04/26/2004
PATENT NUMBER: ISSUE DATE:
TITLE: NEGATIVE DIFFERENTIAL RESISTANCE (NDR) MEMORY CELL WITH REDUCED
SOFT ERROR RATE

SERIAL NUMBER: 10867006 FILING DATE:
PATENT NUMBER: ISSUE DATE:
TITLE:

SERIAL NUMBER: 10884574 FILING DATE:
PATENT NUMBER: ISSUE DATE:
TITLE:

SERIAL NUMBER: 10884576 FILING DATE:
PATENT NUMBER: ISSUE DATE:
TITLE:

SERIAL NUMBER: 09603102 FILING DATE: 06/22/2000
PATENT NUMBER: 6479862 ISSUE DATE: 11/12/2002
TITLE: CHARGE TRAPPING DEVICE AND METHOD FOR IMPLEMENTING A TRANSISTOR
HAVING A NEGATIVE DIFFERENTIAL RESISTANCE MODE

014926/0849 PAGE 4

SERIAL NUMBER: 09603101 FILING DATE: 06/22/2000
PATENT NUMBER: 6512274 ISSUE DATE: 01/28/2003
TITLE: CMOS-PROCESS COMPATIBLE, TUNABLE NDR (NEGATIVE DIFFERENTIAL
RESISTANCE) DEVICE AND METHOD OF OPERATING SAME

SERIAL NUMBER: 10028394 FILING DATE: 12/21/2001
PATENT NUMBER: 6518589 ISSUE DATE: 02/11/2003
TITLE: DUAL MODE FET & LOGIC CIRCUIT HAVING NEGATIVE DIFFERENTIAL
RESISTANCE MODE

SERIAL NUMBER: 10028085 FILING DATE: 12/21/2001
PATENT NUMBER: 6559470 ISSUE DATE: 05/06/2003
TITLE: NEGATIVE DIFFERENTIAL RESISTANCE FIELD EFFECT TRANSISTOR (NDR-FET) &
CIRCUITS USING THE SAME

SERIAL NUMBER: 10185569 FILING DATE: 06/28/2002
PATENT NUMBER: 6567292 ISSUE DATE: 05/20/2003
TITLE: NEGATIVE DIFFERENTIAL RESISTANCE (NDR) ELEMENT AND MEMORY WITH
REDUCED SOFT ERROR RATE

SERIAL NUMBER: 10028089 FILING DATE: 12/21/2001
PATENT NUMBER: 6594193 ISSUE DATE: 07/15/2003
TITLE: CHARGE PUMP FOR NEGATIVE DIFFERENTIAL RESISTANCE TRANSISTOR

SERIAL NUMBER: 09602658 FILING DATE: 06/22/2000
PATENT NUMBER: 6596617 ISSUE DATE: 07/22/2003
TITLE: CMOS COMPATIBLE PROCESS FOR MAKING A TUNABLE NEGATIVE DIFFERENTIAL
RESISTANCE (NDR) DEVICE

SERIAL NUMBER: 10298726 FILING DATE: 11/18/2002
PATENT NUMBER: 6664601 ISSUE DATE: 12/16/2003
TITLE: METHOD OF ORPERATING A DUAL MODE FET& LOGIC CIRCUIT HAVING NEGATIVE
DIFFERENTIAL RESISTANCE MODE

SERIAL NUMBER: 10232129 FILING DATE: 08/30/2002
PATENT NUMBER: 6680245 ISSUE DATE: 01/20/2004
TITLE: METHOD FOR MAKING BOTH A NEGATIVE DIFFERENTIAL RESISTANCE (NDR)
DEVICE AND A NON-NDR DEVICE USING A COMMON MOS PROCESS

SERIAL NUMBER: 10298915 FILING DATE: 11/18/2002
PATENT NUMBER: 6686267 ISSUE DATE: 02/03/2004
TITLE: METHOD FOR FABRICATING A DUAL MODE FET & LOGIC CIRCUIT HAVING
NEGATIVE DIFFERENTIAL RESISTANCE MODE

SERIAL NUMBER: 10315741 FILING DATE: 12/10/2002
PATENT NUMBER: 6686631 ISSUE DATE: 02/03/2004
TITLE: NEGATIVE DIFFERENTIAL RESISTANCE (NDR) DEVICE AND METHOD OF
OPERATING SAME

SERIAL NUMBER: 10298917 FILING DATE: 11/18/2002
PATENT NUMBER: 6693027 ISSUE DATE: 02/17/2004
TITLE: METHOD FOR CONFIGURING A DEVICE TO INCLUDE A NEGATIVE DIFFERENTIAL

014926/0849 PAGE 5

SERIAL NUMBER: 10252906 FILING DATE: 09/23/2002
PATENT NUMBER: 6700155 ISSUE DATE: 03/02/2004
TITLE: CHARGE TRAPPING DEVICE AND METHOD

SERIAL NUMBER: 10298916 FILING DATE: 11/18/2002
PATENT NUMBER: 6724024 ISSUE DATE: 04/20/2004
TITLE: FIELD EFFECT TRANSISTOR PULL-UP/ LOAD ELEMENT

SERIAL NUMBER: 10029077 FILING DATE: 12/21/2001
PATENT NUMBER: 6724655 ISSUE DATE: 04/20/2004
TITLE: MEMORY CELL USING NEGATIVE DIFFERENTIAL RESISTANCE FIELD EFFECT TRANSISTORS

SERIAL NUMBER: 10298700 FILING DATE: 11/18/2002
PATENT NUMBER: 6727548 ISSUE DATE: 04/27/2004
TITLE: NEGATIVE DIFFERENTIAL RESISTANCE (NDR) ELEMENT AND MEMORY WITH REDUCED SOFT ERROR RATE

SERIAL NUMBER: 10028084 FILING DATE: 12/21/2001
PATENT NUMBER: 6754104 ISSUE DATE: 06/22/2004
TITLE: INSULATED-GATE FIELD-EFFECT TRANSISTOR INTEGRATED WITH NEGATIVE DIFFERENTIAL RESISTANCE (NDR) FET

SERIAL NUMBER: FILING DATE:
PATENT NUMBER: ISSUE DATE:
PCT NUMBER: US0119825
TITLE:

SERIAL NUMBER: FILING DATE:
PATENT NUMBER: ISSUE DATE:
PCT NUMBER: US0241333
TITLE:

SERIAL NUMBER: FILING DATE:
PATENT NUMBER: ISSUE DATE:
PCT NUMBER: US0241334
TITLE:

SERIAL NUMBER: FILING DATE:
PATENT NUMBER: ISSUE DATE:
PCT NUMBER: US0319924
TITLE:

SERIAL NUMBER: FILING DATE:
PATENT NUMBER: ISSUE DATE:
PCT NUMBER: US0319925
TITLE:

SERIAL NUMBER: FILING DATE:
PATENT NUMBER: ISSUE DATE:
PCT NUMBER: US0339190

014926/0849 PAGE 6

SERIAL NUMBER:
PATENT NUMBER:
PCT NUMBER: US0339222
TITLE:

FILING DATE:
ISSUE DATE:

SERIAL NUMBER:
PATENT NUMBER:
PCT NUMBER: US0339291
TITLE:

FILING DATE:
ISSUE DATE:

SERIAL NUMBER:
PATENT NUMBER:
PCT NUMBER: US0340267
TITLE:

FILING DATE:
ISSUE DATE:

SERIAL NUMBER:
PATENT NUMBER:
PCT NUMBER: US0340268
TITLE:

FILING DATE:
ISSUE DATE:

SERIAL NUMBER:
PATENT NUMBER:
PCT NUMBER: US0340333
TITLE:

FILING DATE:
ISSUE DATE:

SERIAL NUMBER:
PATENT NUMBER:
PCT NUMBER: US0340360
TITLE:

FILING DATE:
ISSUE DATE:

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ASSIGNMENT DIVISION
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08/02/2004
700103087

Form PTO-1595 (Rev. 06/04)
OMB No. 0851-0027 (exp. 8/30/2005)

U.S. DEPARTMENT OF COMMERCE
United States Patent and Trademark Office

RECORDATION FORM COVER SHEET
PATENTS ONLY

To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

1. Name of conveying party(ies)/Execution Date(s):

Tau-Jae King
David K.Y. Liu

Execution Date(s) _____

Additional name(s) of conveying party(ies) attached? ☐ Yes ☐ No

3. Nature of conveyance:

- ☒ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☐ Government Interest Assignment
☐ Executive Order 9424, Confirmatory License
☐ Other _____

2. Name and address of receiving party(ies)

Name: Progressant Technologies, Inc.

Internal Address: _____

Street Address: 470 Tumbleweed Ct.

City: Fremont

State: California

Country: United States of America Zip: 94539

Additional name(s) & address(es) attached? ☐ Yes ☐ No

4. Application or patent number(s):

☐ This document is being filed together with a new application.

A. Patent Application No.(s)

10/185,247 10/314,735 10/324,486 10/827,713 10/831,950
10/185,559 10/314,785 10/753,948 10/827,787 10/887,008
10/185,568 10/321,031 10/754,229 10/827,888 10/884,574
10/215,137 10/321,090 10/757,245 10/828,356 10/884,576
10/314,510 10/322,080 10/760,090 10/831,807

B. Patent No.(s)

6,479,882 6,587,292 6,880,245 6,700,155 6,754,104
6,512,274 6,594,183 6,686,267 6,724,024
6,518,589 6,596,617 6,686,631 6,724,655
6,559,470 6,684,601 6,693,027 6,727,548

Additional numbers attached? ☒ Yes ☐ No

5. Name and address to whom correspondence concerning document should be mailed:

Name: J. Nicholas Gross

Internal Address: _____

Street Address: 726 Dubois Ave.

City: San Francisco

State: California

Zip: 94117

Phone Number: 415-355-8800

Fax Number: 415-355-9006

Email Address: _____

6. Total number of applications and patents involved:

53

7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40

- ☐ Authorized to be charged by credit card
☒ Authorized to be charged to deposit account
☐ Enclosed
☐ None required (government interest not affecting title)

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a. Credit Card Last 4 Numbers _____
Expiration Date _____

b. Deposit Account Number 501244

Authorized User Name J. Nicholas Gross

9. Signature:

J. Nicholas Gross
Signature
John Nicholas Gross
Name of Person Signing

8/2/04

Date

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Recordation Form Cover Sheet

Continuation of Item 4

4. Application or patent number(s):

A. Patent Application No.(s)

PCT/US01/19825

PCT/US02/41333

PCT/US02/41334

PCT/US03/19924

PCT/US03/19925

PCT/US03/39190

PCT/US03/39222

PCT/US03/39291

PCT/US03/40267

PCT/US03/40268

PCT/US03/40333

PCT/US03/40360